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RUE	4	5/13/97		0,130	Perotto	o, et	al.				
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RUE	7	11/9/99		3339	Klim						
RUZ	8	4/18/00		2708	Flynn,				_		
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)											
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Seattle, WA, no date provided											
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BLE  DEAN M. TULLSEN, et al. "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multihreading Processor" Dept. of Computer Science & Engineering, University of WA, Seattle, WA, no date Provided  ROA P. POKALA, et al., "Physical Synthesis for Performance Optimization", Vertex Semiconductor, San Jose, CA, no date provided  GREGORY T. BYRD, et al., "MULTITHREADED Processor Architectures", Western Carolina University, 8045 IEEE Spectrum, 32(1995) August, No. 8, New York, US											
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Sheet 1 of

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Application Number	09/458,570				
Filing Date	12/09/1999				
First Named Inventor	Dion Rodgers				
Art Unit	2784				
Examiner Name	Not yet assigned				
Attorney Docket Number	042390.P7933				

U.S. PATENT DOCUMENTS								
Examiner Cite		Document Number  Number - Kind Code <sup>2</sup> (if known	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
RLE	1	US- 5,809,522	09-15-1998	Novak et al.				
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Coi	mplete if Known				
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Attorney Docket Number	042390.P7933				

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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